Subject: DLD — Date: 1/12/14

Chapter no 2 (Lecture 5)
Logic Gates

Logic Gates and their Symbols:-

i) AND Gate 
ii) OR Gate 
iii) NOT Gate 
iv) NAND Gate 
v) NOR Gate 
vi) XOR Gate 
vii) XNOR Gate 
} universal gate

I_1	I_2	AND	NAND
0	0	0	1
0	1	0	1
1	0	0	1
1	1	1	0

I_1	I_2	OR	NOR
0	0	0	1
0	1	1	0
1	0	1	0
1	1	1	0

XOR 

I_1	I_2	$I_1 \oplus I_2$	$I_1 \odot I_2$
0	0	0	1
0	1	1	0
1	0	1	0
1	1	0	1

And → "." operator
OR → "+" "
NOT → "¬" operator